



INTEL[®] PARALLEL STUDIO XE 2016 CLUSTER EDITION

For Distributed Performance

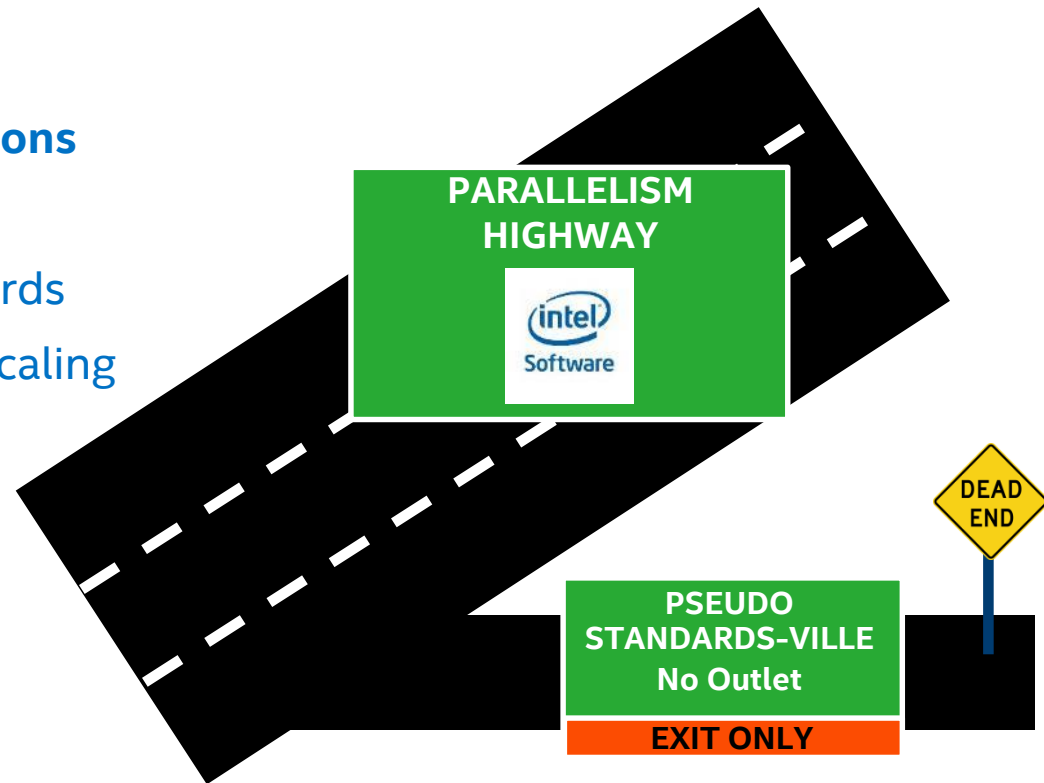
Intel® Parallel Studio XE 2016 development suite - Empowering Faster Code Faster

Delivering HPC Development Solutions

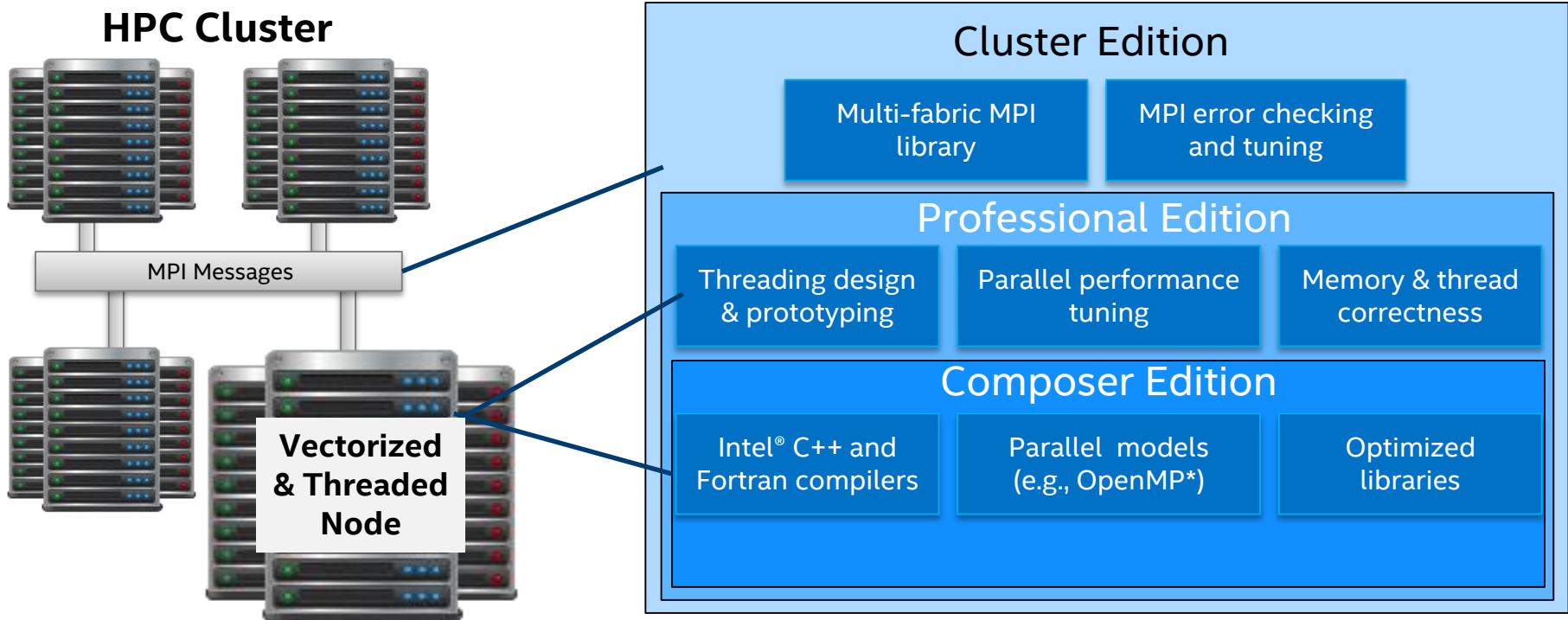
- Over 20 years
- Industry Collaboration on Standards
- Developed with Performance & Scaling with Intel hardware

Meeting the Challenges

- Boosting Performance
- Increasing Scalability
- Increasing Productivity



How Intel® Parallel Studio XE 2016 helps make *Faster Code Faster* for HPC



Intel® MPI Library

Value Proposition

What	<ul style="list-style-type: none">• Intel's High Performance MPI Library
Why	<ul style="list-style-type: none">• Scale Performance – Tuned for Latest Intel Architectures• Scale Forward – Multicore and Manycore Ready• Scale Efficiently – Flexible Fabric Selection & Compatibility
How	<ul style="list-style-type: none">• Standards Based – Built on Open Source MPICH Implementation• Sustained Scalability – Tuning for Low Latencies, High Bandwidth & Increased Processes• Multi Fabric Support – Supports Popular High Performance Networking Fabrics

Intel® MPI Library Overview

Optimized MPI application performance

- Application-specific tuning
- Automatic tuning

Lower latency and multi-vendor interoperability

- Industry leading latency
- Performance optimized support for the latest OFED capabilities through DAPL 2.x

Faster MPI communication

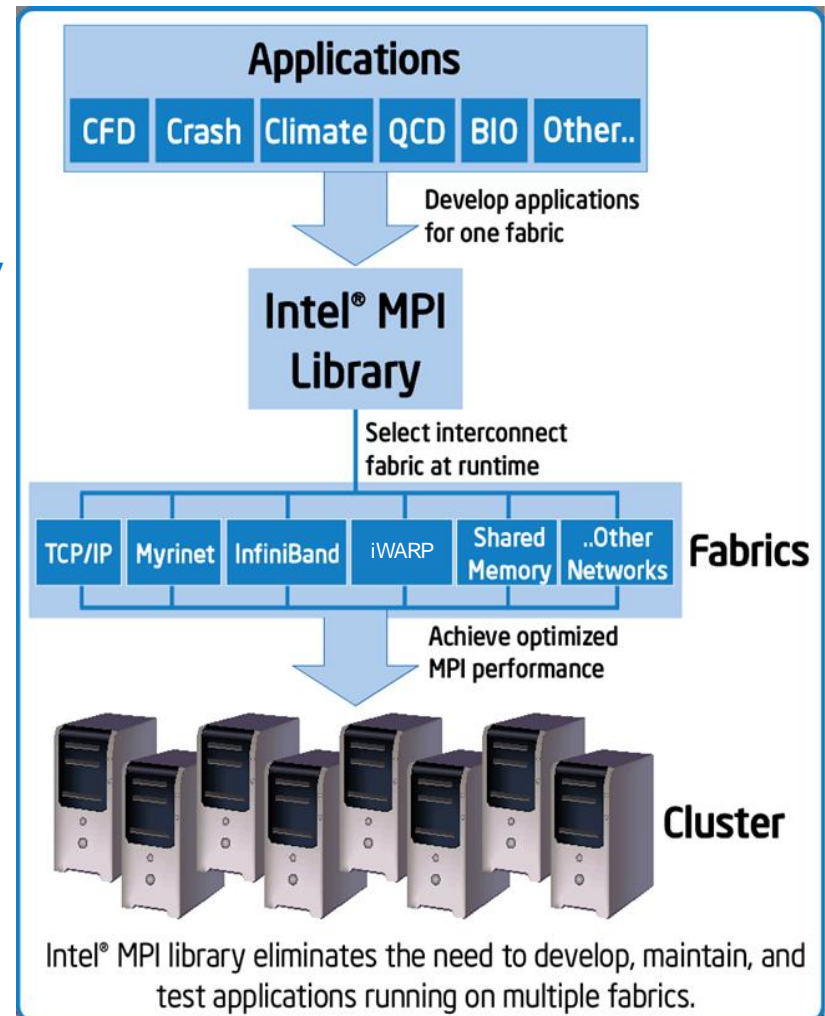
- Optimized collectives

Sustainable scalability beyond 262K cores

- Native InfiniBand* interface support allows for lower latencies, higher bandwidth, and reduced memory requirements

More robust MPI applications

- Seamless interoperability with Intel® Trace Analyzer and Collector

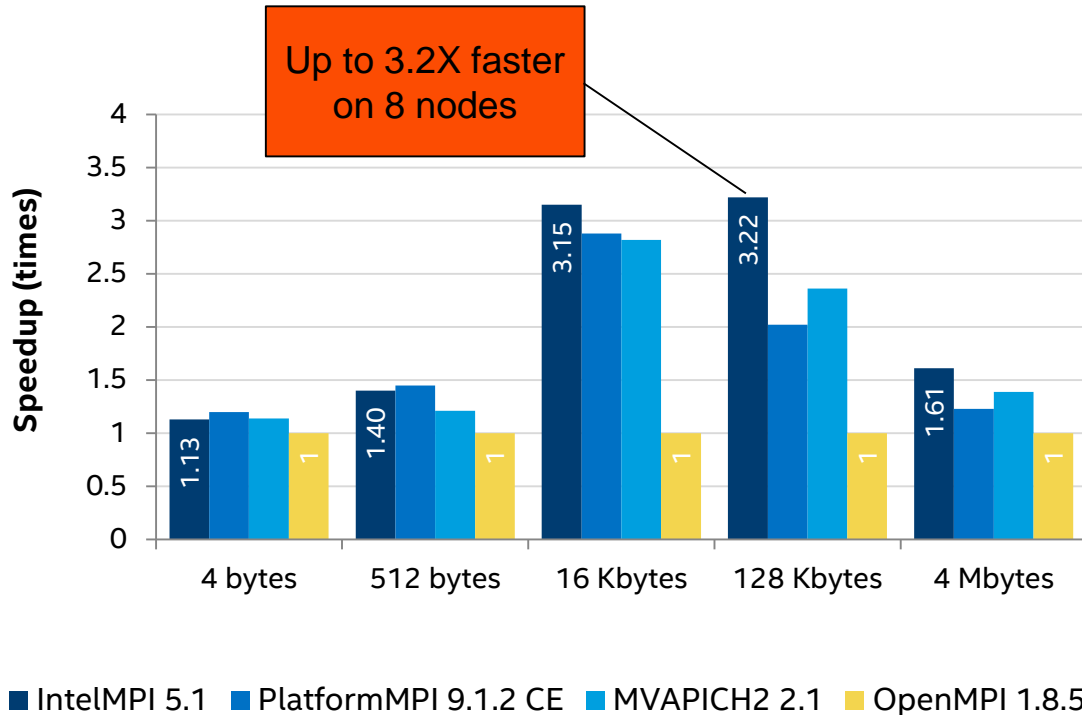


Superior MPI performance on Linux* 64

288 Processes, 8 nodes (InfiniBand + shared memory)

Superior Performance with Intel® MPI Library 5.1

288 Processes, 8 nodes (InfiniBand + shared memory), Linux* 64
Relative (Geomean) MPI Latency Benchmarks
(Higher is Better)



Configuration Info:

Hardware: CPU: Dual Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz; 64 GB RAM. Interconnect: Mellanox Technologies MT27600 [Connect-IB].

Software: RedHat* RHEL 6.5; OFED 3.12-1; Intel® MPI Library 5.1; Intel® MPI Benchmarks 4.1 (built with Intel® C++ Compiler XE 15.0.3 for Linux*);

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. * Other brands and names are the property of their respective owners. Benchmark Source: Intel Corporation.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804

Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved.
*Other names and brands may be claimed as the property of others.

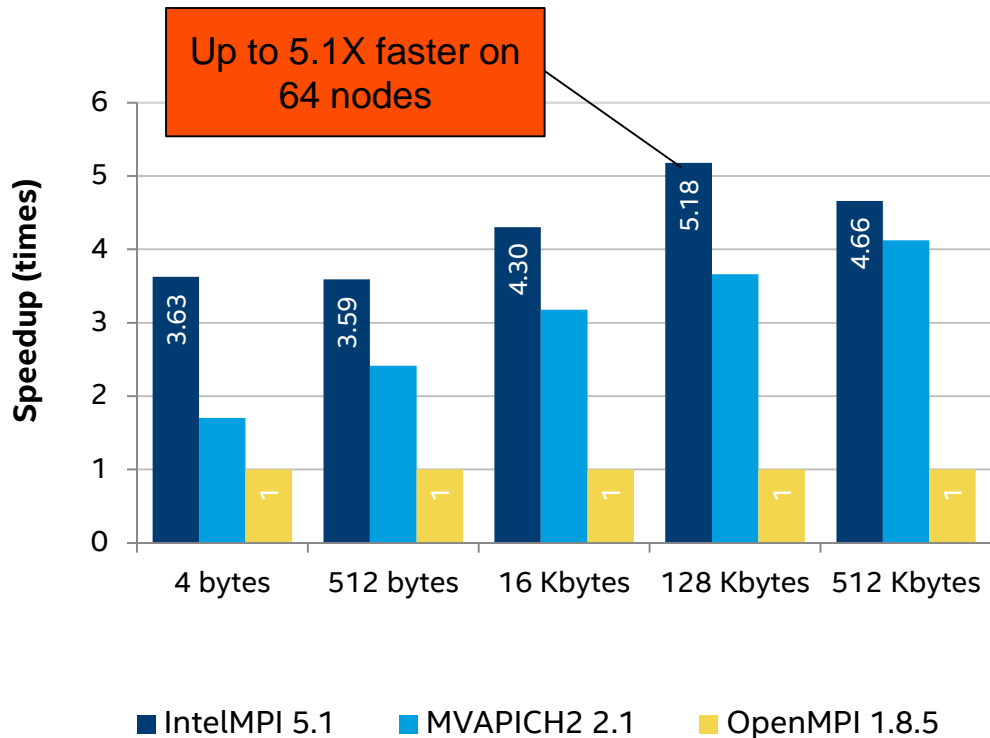


Superior MPI performance on Linux* 64

1792 Processes, 64 nodes (InfiniBand + shared memory)

Superior Performance with Intel® MPI Library 5.1

1792 Processes, 64 nodes (InfiniBand + shared memory), Linux* 64
Relative (Geomean) MPI Latency Benchmarks
(Higher is Better)



Configuration Info:

Hardware: CPU: Dual Intel(R) Xeon(R) CPU E5-2697 v3 @ 2.60GHz; 64 GB RAM. Interconnect: Mellanox Technologies MT27500 Family [ConnectX-3].

Software: RedHat* RHEL 6.5; OFED 3.5-2; Intel® MPI Library 5.1 Intel® MPI Benchmarks 4.1 (Built with Intel® C++ Compiler XE 15.0.3 for Linux*);

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. * Other brands and names are the property of their respective owners. Benchmark Source: Intel Corporation.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804

Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved.
*Other names and brands may be claimed as the property of others.

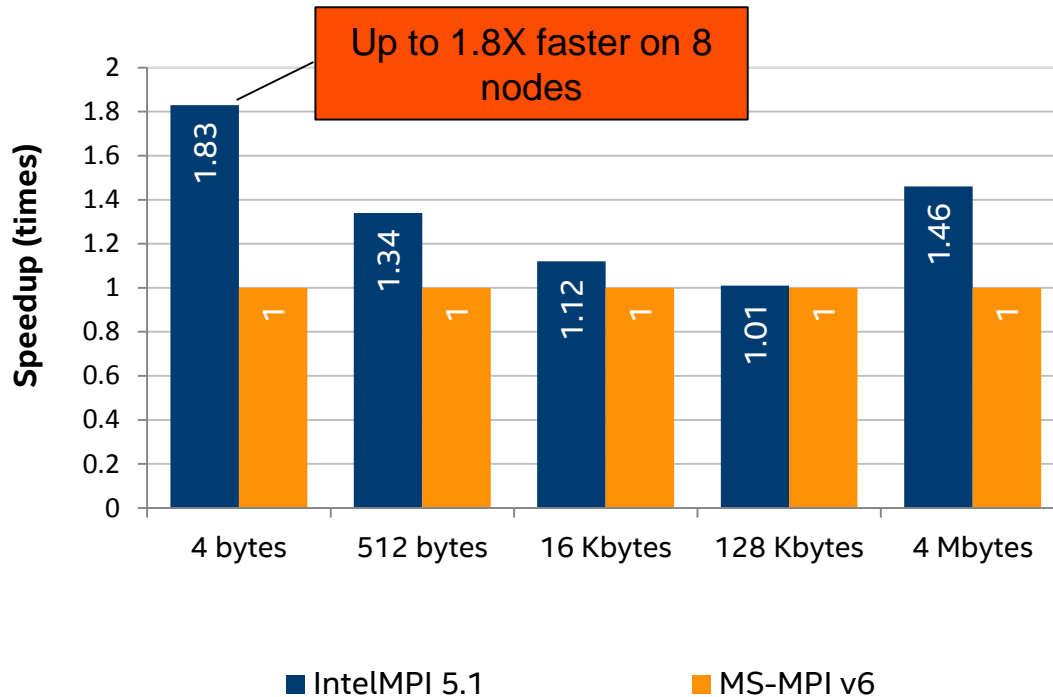


Superior MPI performance on Windows* 64

96 Processes, 8 nodes (IP over InfiniBand + shared memory)

Superior Performance with Intel® MPI Library 5.1

96 Processes, 8 nodes (sock + shared memory), Linux* 64
Relative (Geomean) MPI Latency Benchmarks
(Higher is Better)



Configuration Info:

HOST Hardware: Intel® Xeon® CPU X5680 @ 3.33GHz, RAM 24GB;
Interconnect: InfiniBand, Mellanox ConnectX VPI (MT26428) QDR;
Software: Microsoft Windows Server 2008 HPC Edition, OFED 3.2,
Intel® C/C++ Compiler XE 15.0.3, Intel® MPI Benchmarks 4.1.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. * Other brands and names are the property of their respective owners. Benchmark Source: Intel Corporation.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804

Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved.
*Other names and brands may be claimed as the property of others.

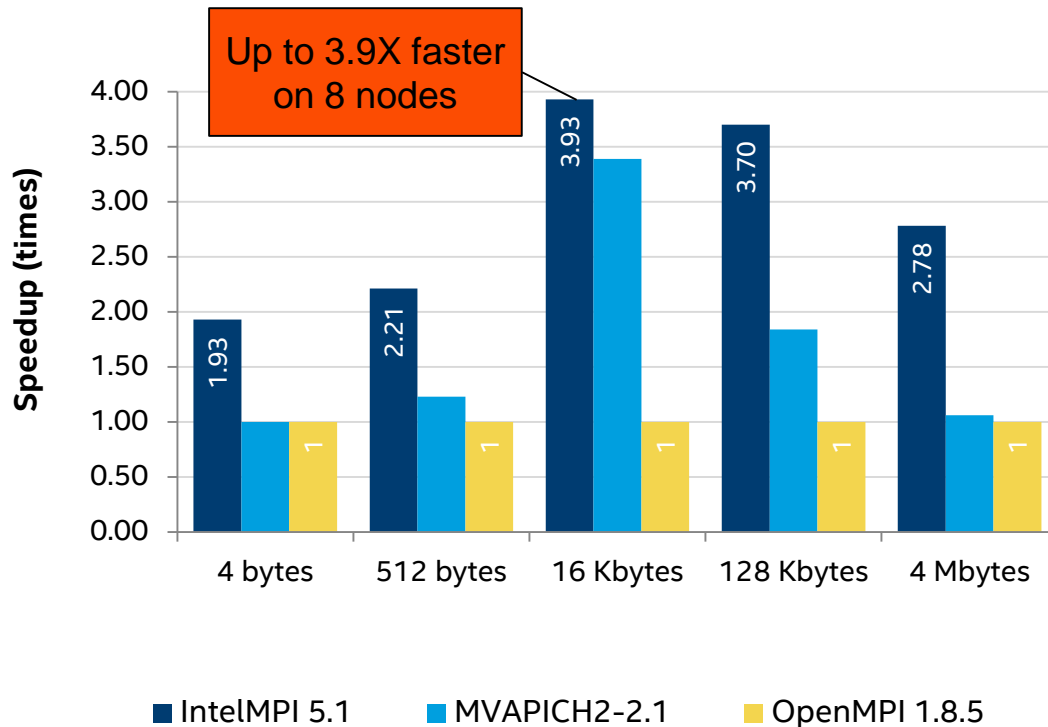


Superior MPI performance on Xeon Phi™

64 Processes, 8 nodes (InfiniBand + shared memory)

Superior Performance with Intel® MPI Library 5.1

64 Processes, 8 nodes (InfiniBand + shared memory), Linux* 64
Relative (Geomean) MPI Latency Benchmarks
(Higher is Better)



Configuration Info:

HOST Hardware: Intel® Xeon® CPU E5-2680 @ 2.70GHz, RAM 64GB;
Interconnect: InfiniBand, Mellanox Technologies MT27500 Family [ConnectX-3];

MIC Hardware: Intel® Xeon Phi™ coprocessor SE10/7120 series (rev 20) 1238095 kHz; 61 cores. RAM: 15872 MB per card.

Software: RHEL 6.5, OFED 3.12-1, MPSS Version: 3.5, Intel® C/C++ Compiler XE 15.0.3, Intel® MPI Benchmarks 4.1.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. * Other brands and names are the property of their respective owners. Benchmark Source: Intel Corporation.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Notice revision #20110804

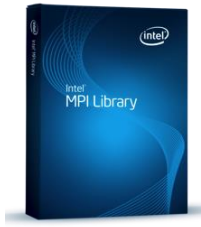
Optimization Notice

Copyright © 2015, Intel Corporation. All rights reserved.
*Other names and brands may be claimed as the property of others.



Intel® MPI Library 5.1

What's New



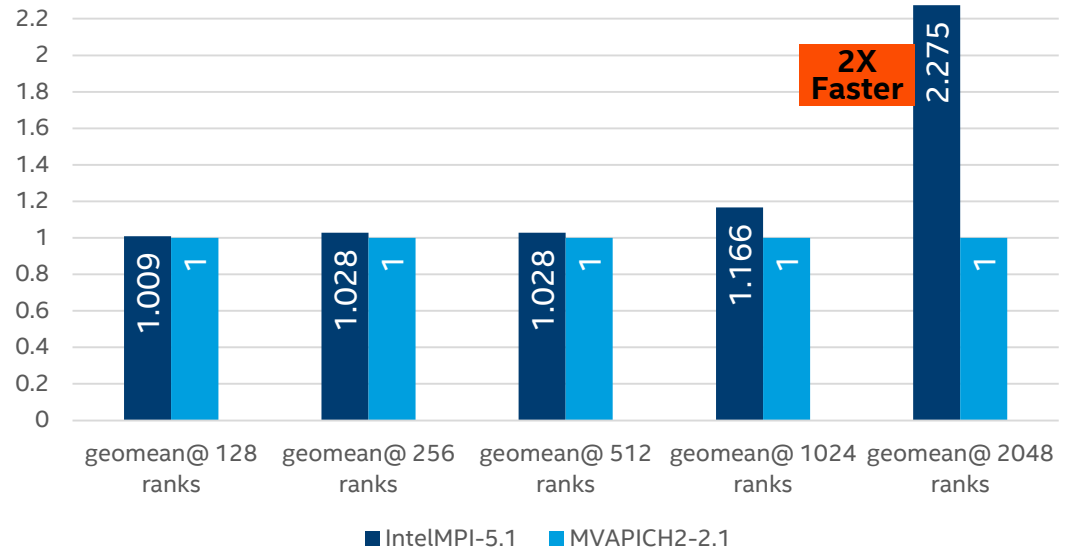
Ease of use

- Brand new Troubleshooting section for quicker issue resolution

Performance & Scaling

- Support for OpenFabrics* Interface (OFI*) v1.0
- Additional features for faster automatic tuning

Application performance at scale
(Dual Xeon™ E5-2697 v3 @ 2.60GHz, -ppn 24)



Based on the following set of tests: SpecMPI-2.0/104.milc, SpecMPI-2.0/127.wrf2 (Built with Intel® C++ Compiler XE 15.0.3.187 for Linux*); Hardware: CPU: Dual Intel® Xeon E5-2697v3@2.60GHz; 64 GB RAM. Interconnect: Mellanox Technologies* MT27500 Family [ConnectX*-3] FDR. Software: RedHat® RHEL 6.5; OFED 3.5-2-MIC-rc3; Intel® MPI Library 5.1, MVAPICH2-2.1. Environment: MV2_IBA_HCA=mlx4_0(mvapich2 only).

Performance Tuning Tools for Distributed Applications

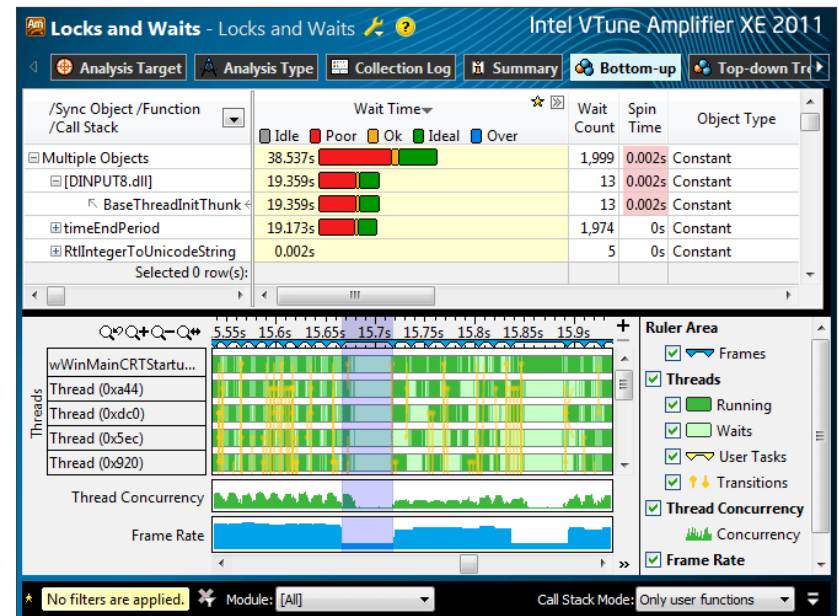
Intel® Trace Analyzer and Collector



Tune cross-node MPI

- Visualize MPI behavior
- Evaluate MPI load balancing
- Find communication hotspots

Intel® VTune™ Amplifier XE



Tune single node threading

- Visualize thread behavior
- Evaluate thread load balancing
- Find thread sync bottlenecks

Intel® Trace Analyzer and Collector

Value Proposition

What	<ul style="list-style-type: none">• Intel's High Performance MPI Communications Profiler & Analyzer for Scalable HPC Development
Why	<ul style="list-style-type: none">• Scale Performance – Perform on More Nodes• Scale Forward – Multicore and Manycore Ready• Scale Efficiently – Tune & Debug on More Nodes
How	<ul style="list-style-type: none">• Visualize - Understand parallel application behavior• Evaluate - Profiling statistics and load balancing• Analyze – Automated analysis of common MPI issues• Identify – Communication hotspots

Intel® Trace Analyzer and Collector Overview

Intel® Trace Analyzer and Collector helps the developer:

- Visualize and understand parallel application behavior
- Evaluate performance and load balancing
- Identify communication hotspots

API and *-tcollect*

-trace

Features

- Event-based approach
- Low overhead
- Excellent scalability
- Powerful aggregation and filtering
- Performance Assistance and Imbalance Tuning
- NEW in 9.1: MPI Performance Snapshot

Intel® Trace Collector

Trace File (.stf)

Intel® Trace Analyzer

Source Code

Compiler

Objects

Linker

Binary

Runtime

Output

Using the Intel® Trace Analyzer and Collector is ... Easy!

Step 1

Run your binary and create a tracefile

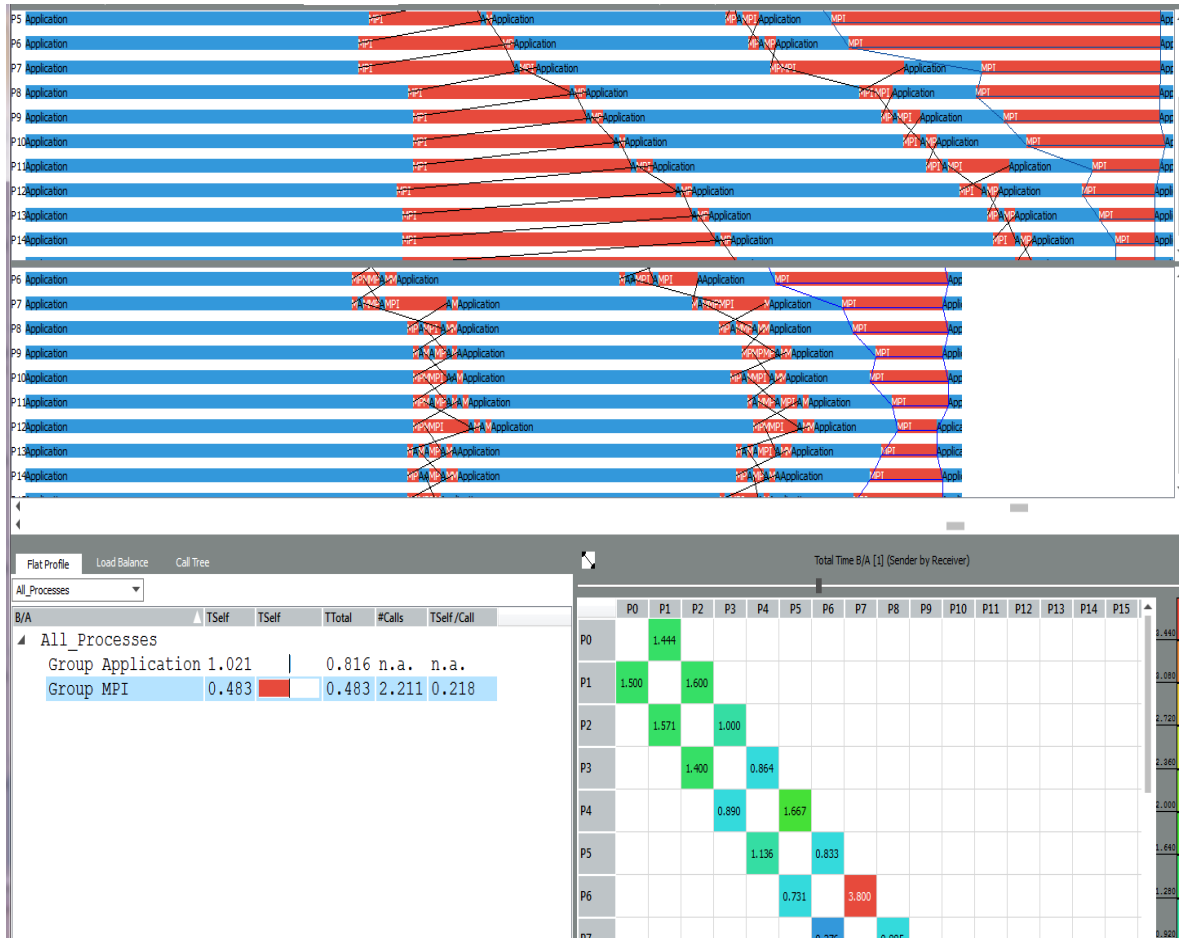
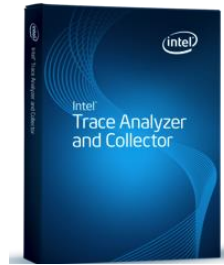
```
$ mpirun -trace -n 2 ./test
```

Step 2

View the Results:

```
$ traceanalyzer &
```

Intel® Trace Analyzer and Collector



Compare the event timelines of two communication profiles

Blue = computation
Red = communication

Chart showing how the MPI processes interact

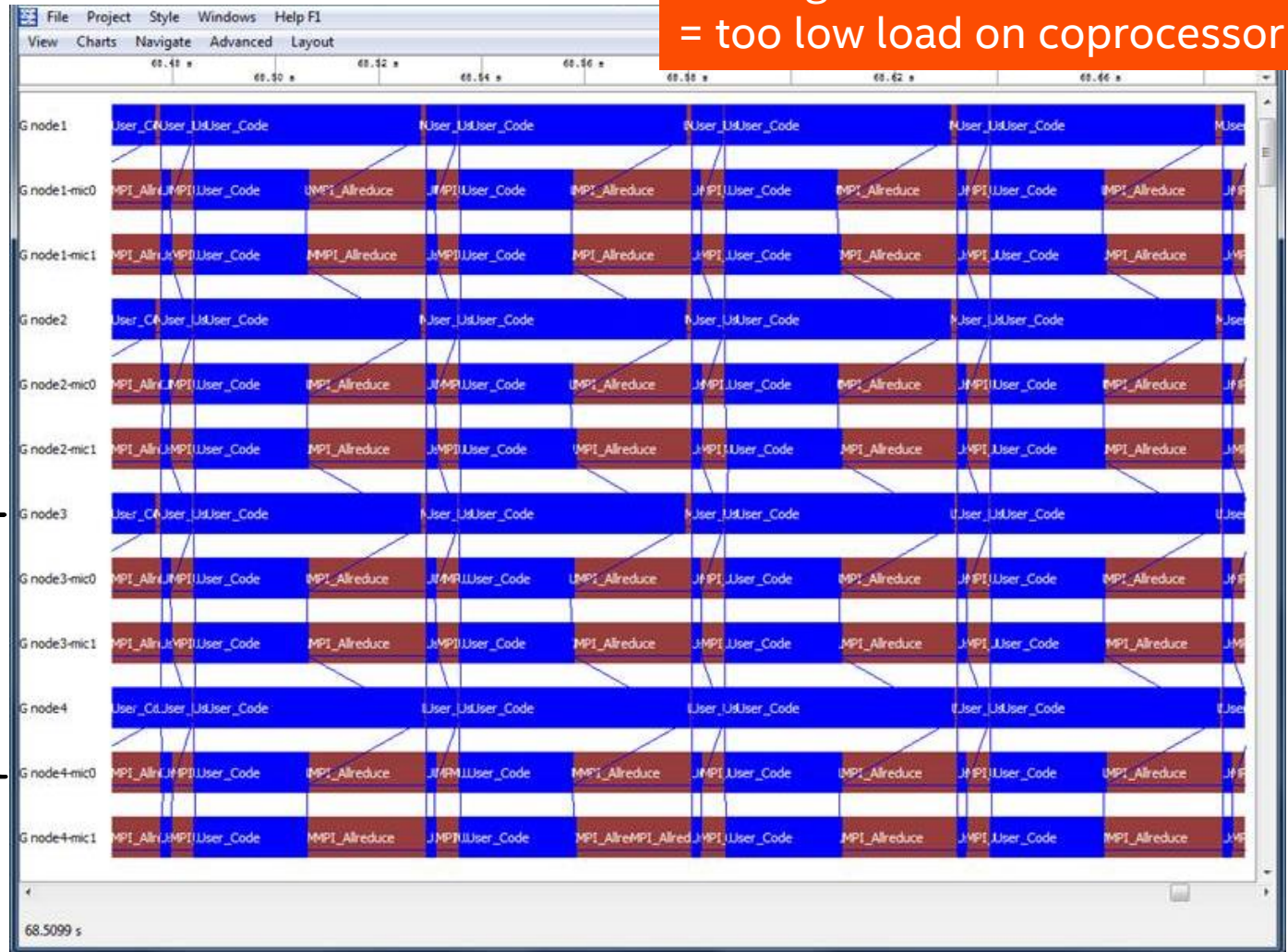
Improving Load Balance: Real World Case

Too high load on Host
= too low load on coprocessor

Collapsed data per node and coprocessor card

Host
16 MPI procs x
1 OpenMP thread

Coprocessor
8 MPI procs x
28 OpenMP threads



Improving Load Balance: Real World Case

Too low load on Host
= too high load on coprocessor

Collapsed data per node and coprocessor card

Host
16 MPI procs x
1 OpenMP thread

Coprocessor
24 MPI procs x
8 OpenMP threads



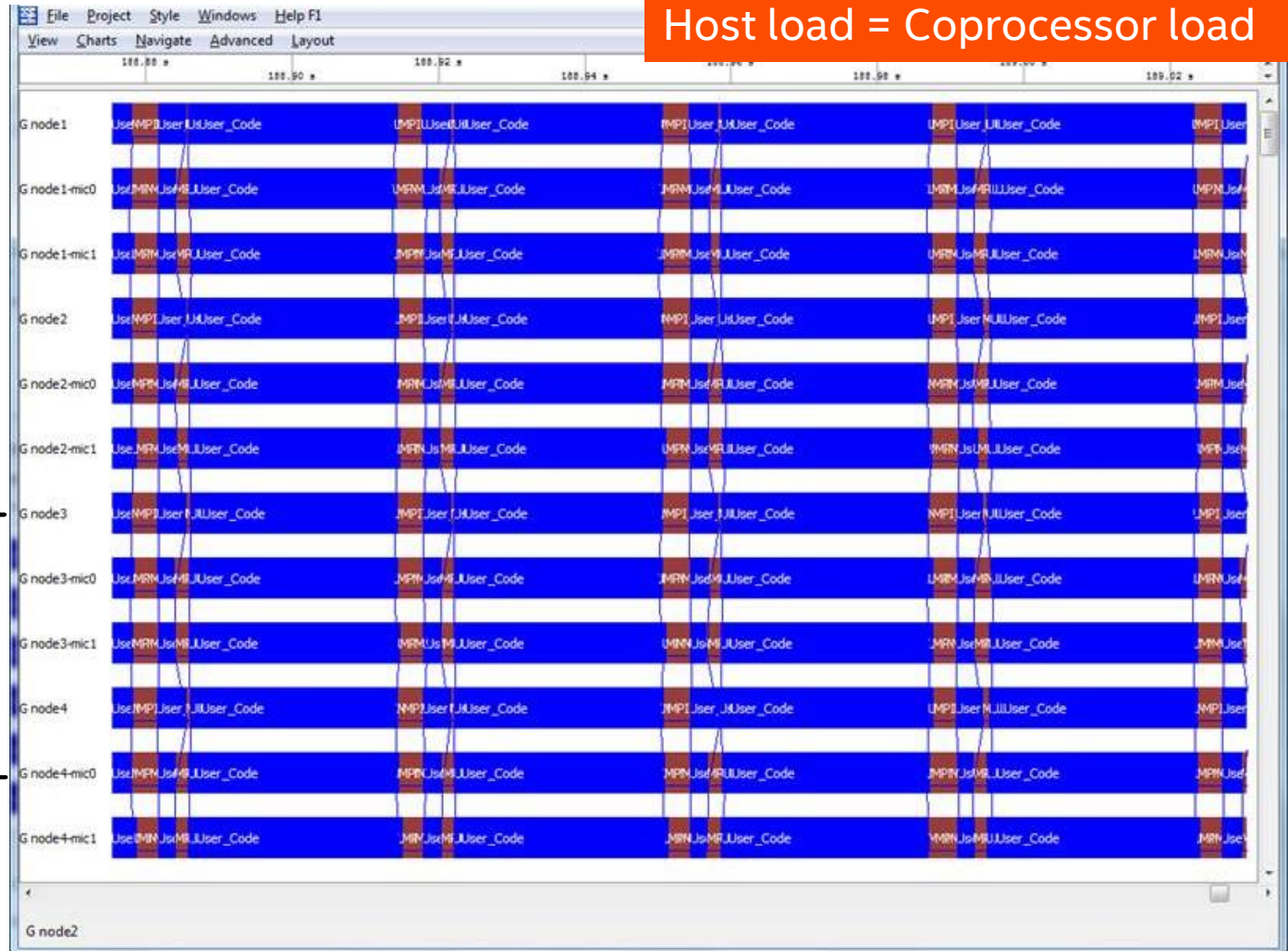
Improving Load Balance: Real World Case

Perfect balance
Host load = Coprocessor load

Collapsed data per node and coprocessor card

Host
16 MPI procs x
1 OpenMP thread

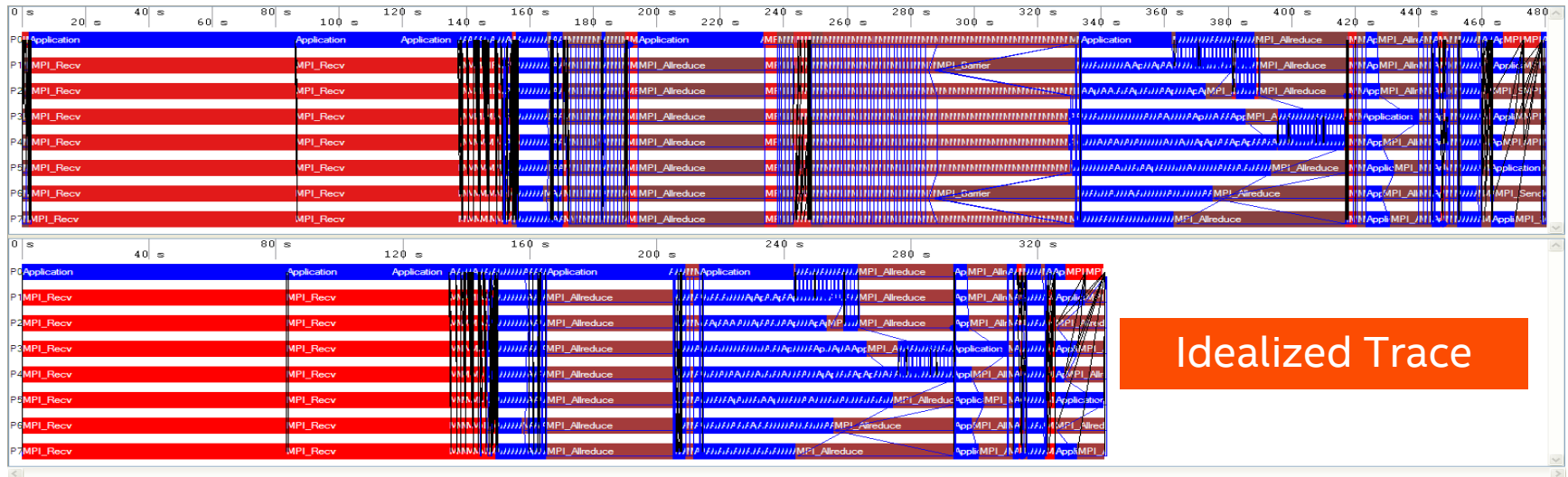
Coprocessor
16 MPI procs x
12 OpenMP thrds



Ideal Interconnect Simulator (Idealizer)

Helps to figure out application's imbalance simulating its behavior in the "ideal communication environment"

Actual trace



Idealized Trace

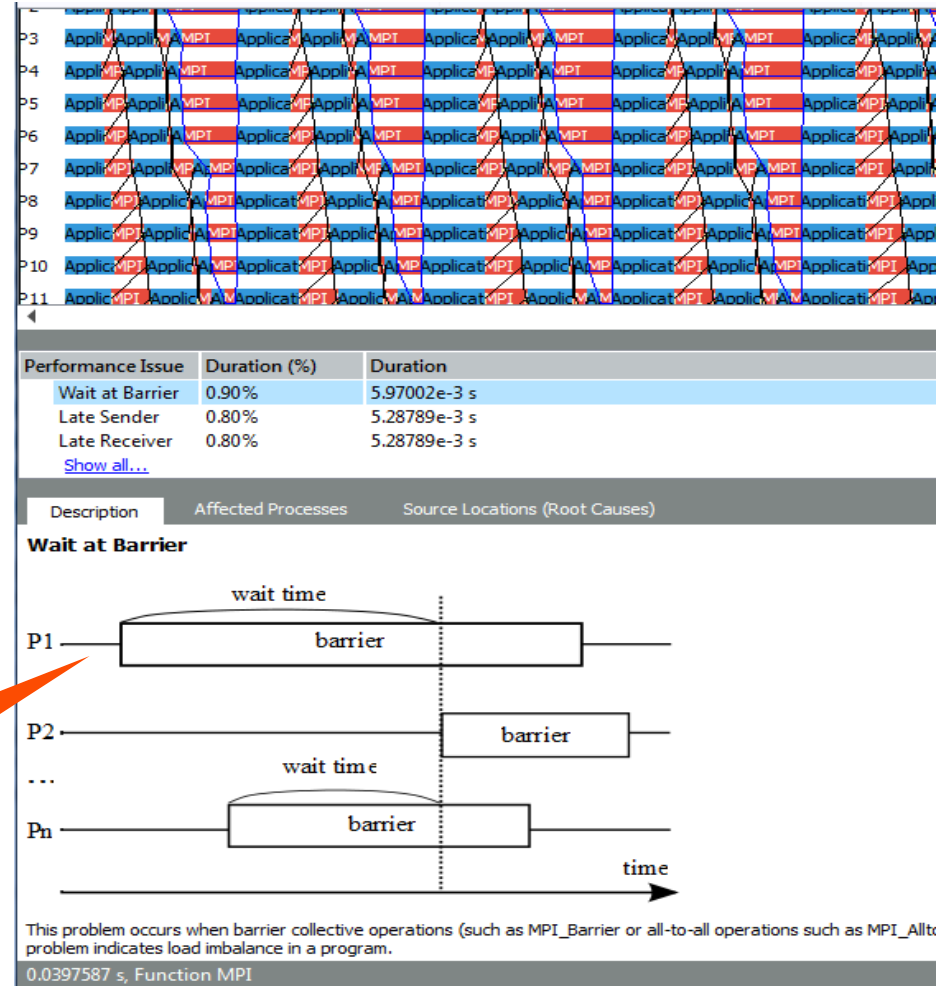
Easy way to identify application bottlenecks

MPI Performance Assistance

Automatic Performance Assistant

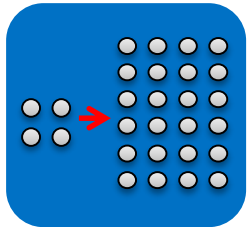
- Detect common MPI performance issues
- Automated tips on potential solutions

Automatically detect performance issues and their impact on runtime

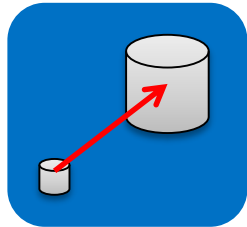


MPI Performance Snapshot

High capacity MPI profiler



Lightweight
Low overhead profiling
for 100K+ Ranks



Scalability
Performance variation at
scale can be detected
sooner



Identifying Key Metrics
Shows PAPI counters
and MPI/OpenMP
imbalances

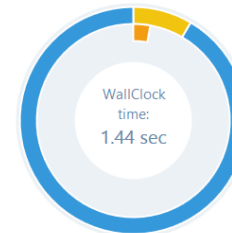
MPI Performance Snapshot Summary



Application: ./poisson
Number of ranks: 32
Used statistics: stats.txt, app_stat.txt

Overview

■ MPI Time: 0.12 sec 8.16%
■ MPI Imbalance: 0.04 sec 2.80%
■ Computation Time: 1.30 sec 91.84%



Memory Usage

■ Peak memory consumption (rank 1): 13.18 MB

Performance by Metric

■ WallClock time: 1.44 sec
Total application lifetime. The time is elapsed time for the slowest process. This metric includes the MPI Time and the Computation time below.

■ MPI Time: 0.12 sec 8.16%
Time spent inside the MPI library. High values are usually bad. This value is **LOW**. The application did NOT spend much time inside the MPI library.

■ MPI Imbalance: 0.04 sec 2.80%
Mean unproductive wait time per process spent in the MPI library calls when a process is waiting for data. This time is part of the MPI time above. High values are usually bad. This value is **LOW**. The application workload is **well balanced** between MPI ranks.

■ Computation Time: 1.30 sec 91.84%
Mean time per process spent in the application code. This is the sum of the OpenMP Time and the Serial time. High values are usually good. This value is **HIGH**. The application is **Computation-bound**. [More details...](#)

```

----- GENERAL STATISTICS -----
WallClock:      284.274 sec (All processes)
  MIN:          31.998 sec (rank 0)
  MAX:          35.534 sec (rank 7)

----- HW COUNTERS STATISTICS -----
GFlops:    9.563  MPI: 11.28%  NON_MPI: 88.72%

Floating-Point instructions: 45.77%
Vectorized DP instructions: 24.69%
Memory access instructions: 42.35%

----- MEMORY USAGE STATISTICS -----
All processes: 256.740MB
  MIN:         30.608MB (process 7)
  MAX:         33.136MB (process 1)
    
```

```

MPI IMBALANCE STATISTICS -----
207.847 sec          73,12% (All process)
23.044 sec           64,85% (rank 6)
30.113 sec           88,57% (rank 1)
    
```

```

OpenMP STATISTICS -----
228.631 sec          80,43%          56 r
25.348 sec          71,33%          7 r
MAX:                33.124 sec          97,42%          7 r
    
```

```

OpenMP Imbalance:    103.924 sec          36,56% (All proce
  MIN:               11.522 sec          32,43% (rank 3)
  MAX:               15.057 sec          44,29% (rank 2)
    
```


Intel® MPI Benchmarks 4.1

Standard benchmarks with OSI-compatible CPL license

- Enables testing of interconnects, systems, and MPI implementations
- Comprehensive set of MPI kernels that provide performance measurements for:
 - Point-to-point message-passing
 - Global data movement and computation routines
 - One-sided communications
 - File I/O
 - Supports MPI-1.x, MPI-2.x, and MPI-3.x standards

What's New:

Introduction of new benchmarks

- Measure cumulative bandwidth and message rate values

The Intel® MPI Benchmarks provide a simple and easy way to measure MPI performance on your cluster

Online Resources

Intel® MPI Library product page

- www.intel.com/go/mpi

Intel® Trace Analyzer and Collector product page

- www.intel.com/go/traceanalyzer

Intel® Clusters and HPC Technology forums

- <http://software.intel.com/en-us/forums/intel-clusters-and-hpc-technology>

Intel® Xeon Phi™ Coprocessor Developer Community

- <http://software.intel.com/en-us/mic-developer>

Legal Disclaimer & Optimization Notice

INFORMATION IN THIS DOCUMENT IS PROVIDED "AS IS". NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO THIS INFORMATION INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products.

Copyright © 2015, Intel Corporation. All rights reserved. Intel, Pentium, Xeon, Xeon Phi, Core, VTune, Cilk, and the Intel logo are trademarks of Intel Corporation in the U.S. and other countries.

Optimization Notice

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804

